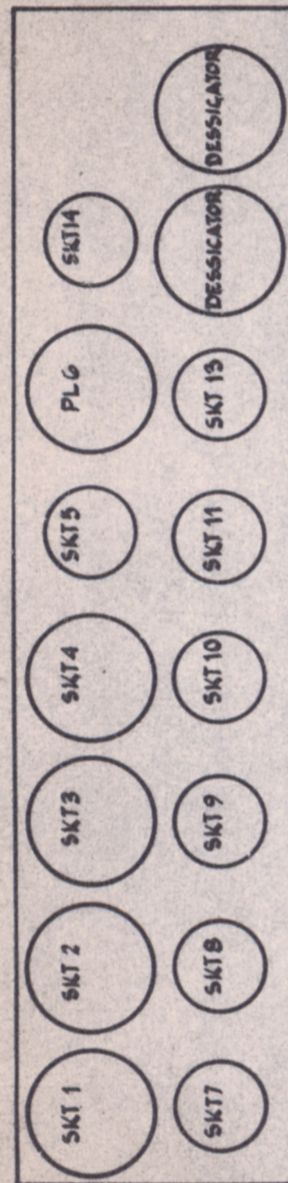


THIRD ANGLE PROJECTION

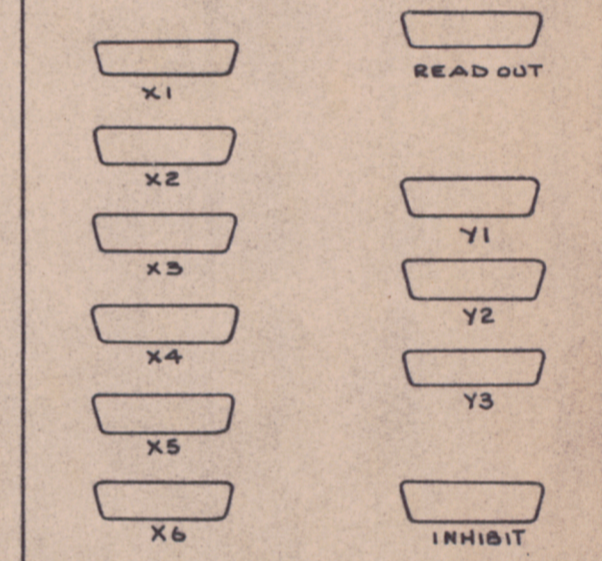
DIMENSIONS IN INCHES UNLESS OTHERWISE STATED

DO NOT SCALE DRAWING

- SKT. 1 DISPLAY UNIT PL1.
- SKT. 2 DISPLAY UNIT PL2.
- SKT. 3 DISPLAY UNIT PL3.
- SKT. 4 INPUT OUTPUT UNIT (DATA)
- SKT. 5 CONTROL UNIT PL1
- SKT. 7 MARGINAL TEST UNIT PL1
- SKT. 8 ADDITIONAL STORE
- SKT. 9 ADDITIONAL STORE
- SKT. 10 PAPER TAPE CONTROLLER PL2
- SKT. 11 CONTROL UNIT. PL2.
- PL. 5. COMPUTER POWER SUPPLY SKT2.
- SKT. 13. INPUT OUTPUT UNIT (CONTROL)
- SKT. 14. EXTRA STORE



1	A - FT CONTROL KEY INPUT	32	33	SPARE	71	A - GZ TEMP SENSING & CONTROL	32
2	A - FL MANUAL CONTROL LOGIC	32	33	A - FB WAVEFORM AMPLIFIERS	32	A - EA3 INHIBIT DRIVER & SENSE AMP	40
3	A - FD PROGRAM INTERRUPT	32	33	A - GL INITIAL INSTRUCTIONS 2	32	A - EA3 INHIBIT DRIVER & SENSE AMP	40
4	A - FC TIMING	32	33	A - GK INITIAL INSTRUCTIONS 1	32	A - EA3 INHIBIT DRIVER & SENSE AMP	40
5	A - FR TIMING WAVEFORM DRIVERS	32	33	A - FA REGISTERS (BIT 18)	32	A - EA3 INHIBIT DRIVER & SENSE AMP	40
6	A - FL CONTROL FUNCTION DRIVE	32	33	A - FA REGISTERS (BIT 17)	32	A - EA3 INHIBIT DRIVER & SENSE AMP	40
7	A - FH CONTROL MATRIX	32	33	A - FA REGISTERS (BIT 16)	32	A - EA3 INHIBIT DRIVER & SENSE AMP	40
8	A - FB CONTROL MATRIX AMPLIFIERS 1	32	33	A - FA REGISTERS (BIT 15)	32		
9	A - FB CONTROL MATRIX AMPLIFIERS 2	32	33	A - FA REGISTERS (BIT 14)	32		
10	A - GJ CONTROL MATRIX (FN 15)	32	33	A - FA REGISTERS (BIT 13)	32		
11	A - GI CONTROL MATRIX (FN 14A)	32	33	A - FA REGISTERS (BIT 12)	32		
12	A - GH CONTROL MATRIX (FN 14)	32	33	A - FA REGISTERS (BIT 11)	32		
13	A - GG CONTROL MATRIX (FN 13)	32	33	A - FA REGISTERS (BIT 10)	32		
14	A - GF CONTROL MATRIX (FN 12)	32	33	A - FK WAVEFORM DRIVERS 2	32		
15	A - GE CONTROL MATRIX (FN 5.7.8)	32	33	A - FJ WAVEFORM DRIVERS 1	32		
16	A - GD CONTROL MATRIX (FN 5.6.9)	32	33	A - FA REGISTERS (BIT 9)	32		
17	A - GC CONTROL MATRIX (FNS 2.10.1)	32	33	A - FA REGISTERS (BIT 8)	32		
18	A - GB CONTROL MATRIX (FNS 0.13A)	32	33	A - FA REGISTERS (BIT 7)	32		
19	A - GA MATRIX (CONTROL CHAIN)	32	33	A - FA REGISTERS (BIT 6)	32		
20	A - FG CONTROL MATRIX ADDRESS DRIVER	32	33	A - FA REGISTERS (BIT 5)	32		
21	A - FF CONTROL MATRIX ADDRESS DRIVER	32	33	A - FA REGISTERS (BIT 4)	32		
22	A - FE FUNCTION DECODE TEST	32	33	A - FA REGISTERS (BIT 3)	32		
23	A - FP PROCESS COUNTER (BD.1)	32	33	A - FA REGISTERS (BIT 2)	32		
24	A - FP PROCESS COUNTER (BD.2)	32	33	A - FA REGISTERS (BIT 1)	32		
25	A - FP PROCESS COUNTER (BD.3)	32	33	A - FM STORE CONTROL & OVERFLOW LOGIC	32		
26	* A - FV CABLE RECEIVERS 1	32	33	A - EB3 STORE CONTROL CHAIN UNIT	40		
27	* A - FU CABLE TRANSMITTERS	32	33	A - EC3 MASTER DRIVERS	40		
28	A - FO CABLE RECEIVERS 1	32	33	A - ED3 DRIVER & GATES	40		
29	A - FS CABLE RECEIVERS 2	32	33	A - ED3 DRIVERS & GATES	40		
30	A - FQ INPUT, OUTPUT, CONTROL	32	33	A - ED3 DRIVERS & GATES	40		
31	A - FN CABLE TRANSMITTERS 1	32	33	A - ED3 DRIVERS & GATES	40		
32	A - FN CABLE TRANSMITTERS 2	32	33	A - ED3 DRIVERS & GATES	40		



VIEW ON BACK WIRING SIDE

* ONLY SUPPLIED WITH EXTRA STORE UNITS

A3

LATEST M.I INCORPORATED M.I 2998

ISSUE No.	1	2	3
ALTERATION No.		1542	1759
DATE	20.5.66	25.7.66	10.11.66
INITIALS	T.E.B.	L.F.L.	L.F.L.
PARTS LIST No.			
DRAWN	T.R.B.	CHECKED	APPROVED
			DATE

MATERIAL	FINISH
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ELLIOTT BROTHERS (LONDON) LTD		SCALE
TITLE MSC 920B COMPUTER CONNECTOR AND UNIT LAYOUT	DRAWING No. 322 C 7432	